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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10086129	FILING DATE 02/28/2002	CLASS 714	SUBCLASS 733	GAU 2133	EXAMINER <i>Decady</i>
**APPLICANTS: Schultz David; <i>RSD</i> <i>KERNEROS</i>					
**CONTINUING DATA VERIFIED: <i>NONE</i> <i>6/19/2004</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>NONE</i> <i>6/19/2004</i>					
PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO X-1069 US	
Verified and Acknowledged Examiners's initials <i>[initials]</i>					
TITLE : Method and system for flexible nesting JTAG TAP controllers for FPGA-based system-on-chip (SoC)					
U.S. DEPT. OF COMM /PAT. & TM-PTO-436 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs.Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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